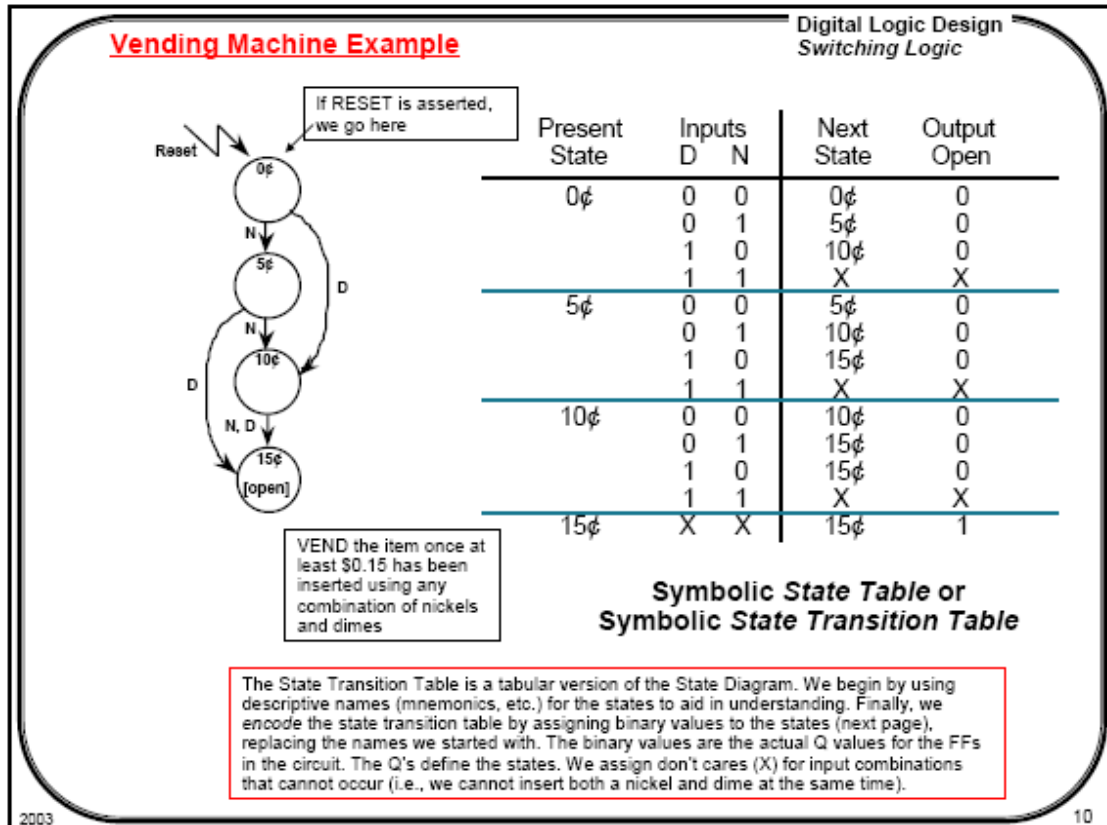


Building a State Machine

Given the following fully specified state machine, we will come up with the logic to drive it. The vending machine below has four states. One for each possible status – 0, 5, 10, and 15 cents. Once fifteen cents is reached, the machine opens. Also, the machine assumes that the machine cannot be given a nickel and a dime at the same time.



Next each state is given a unique binary number and placed back in the table.

Digital Logic Design
Switching Logic

Vending Machine Example

State Encoding

Present State		Inputs		Next State		Output
Q ₁	Q ₀	D	N	D ₁	D ₀	Open
0	0	0	0	0	0	0
		0	1	0	1	0
		1	0	1	0	0
		1	1	X	X	X
0	1	0	0	0	1	0
		0	1	1	0	0
		1	0	1	1	0
1	0	0	0	X	X	X
		0	1	1	0	0
		1	0	1	1	0
		1	1	X	X	X
1	1	0	0	1	1	1
		0	1	1	1	0
		1	0	1	1	0
		1	1	X	X	X

Assign binary state encodings*

Encoded State Transition Table

*For example, if we are in state [10] then \$0.10 has been inserted. If we are in state [11] then at least \$0.15 has been inserted and we assert the VEND signal to release the item. State [00] is the reset (starting) state.

If D flip-flops are used, the Next State columns represent the *excitation* that must be applied to the D inputs to cause the flip-flops to transition to the desired next state upon application of the clock signal.

$Q \leftarrow D$

We often write this as

$Q^+ \leftarrow D$

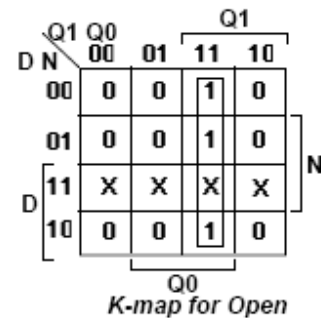
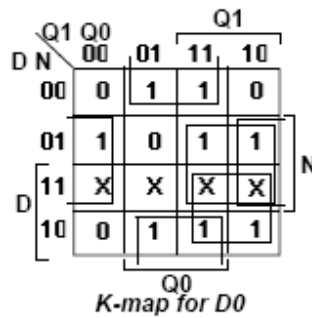
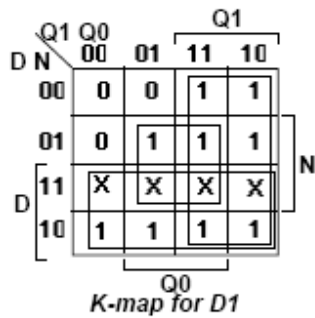
To distinguish the Next State from the Present State.

From here, we must figure out feedback logic for how the next state will be decided. This is done with k-maps. In essence, we have four inputs that determine the next state - D, N, Q1, and Q0. The outputs for the four inputs are D1 and D0.

The k-maps are worked out on the next page. Also, the output variable **open** is worked out using the present state as the input. You may notice quickly, however, that **open** is logic '1' only when the present state is "11" or Q1Q0.

Vending Machine Example

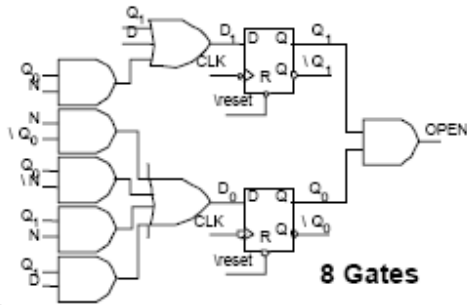
Determine minimized excitation equations based on desired transitions



$$D1 = Q1 + D + Q0 N$$

$$D0 = N \bar{Q0} + Q0 \bar{N} + Q1 N + Q1 D$$

$$OPEN = Q1 Q0$$



The present state (Q1 and Q0) and the nickel (N) and dime (D) signals are the inputs. We must design logic that takes these inputs and computes the excitation logic for the two D FF inputs (D1 and D0) necessary to produce the next state values (Q1+ and Q0+). The K-maps are used to determine the minimized logic for this operation. The circuit will now move to the correct next state given the present state (Q1 and Q0) and inputs N and D.